

FIG. 1

FIG. 2 is a schematic diagram of a dimmer switch system in accordance with the present invention. The system includes a main panel 12, a single room controller 10, and three dimmer switches 20, 22, and 24. The main panel 12 includes a 120VAC circuit breaker 16, a line A terminal 14, a line B terminal 18, and a neutral terminal 16. The single room controller 10 includes a line terminal 32, a neutral terminal 34, and a dimmer switch 36. The dimmer switches 20, 22, and 24 each include a line terminal, a neutral terminal, and a load terminal. The line A terminal 14 is connected to the line terminal 32 of the single room controller 10. The line B terminal 18 is connected to the line terminal of each of the dimmer switches 20, 22, and 24. The neutral terminal 16 is connected to the neutral terminal 34 of the single room controller 10 and the neutral terminal of each of the dimmer switches 20, 22, and 24. The load terminal of each of the dimmer switches 20, 22, and 24 is connected to a load 26, 28, and 30, respectively. The load 26 is connected to the line terminal 32 of the single room controller 10. The load 28 is connected to the line terminal 34 of the single room controller 10. The load 30 is connected to the line terminal 36 of the single room controller 10.

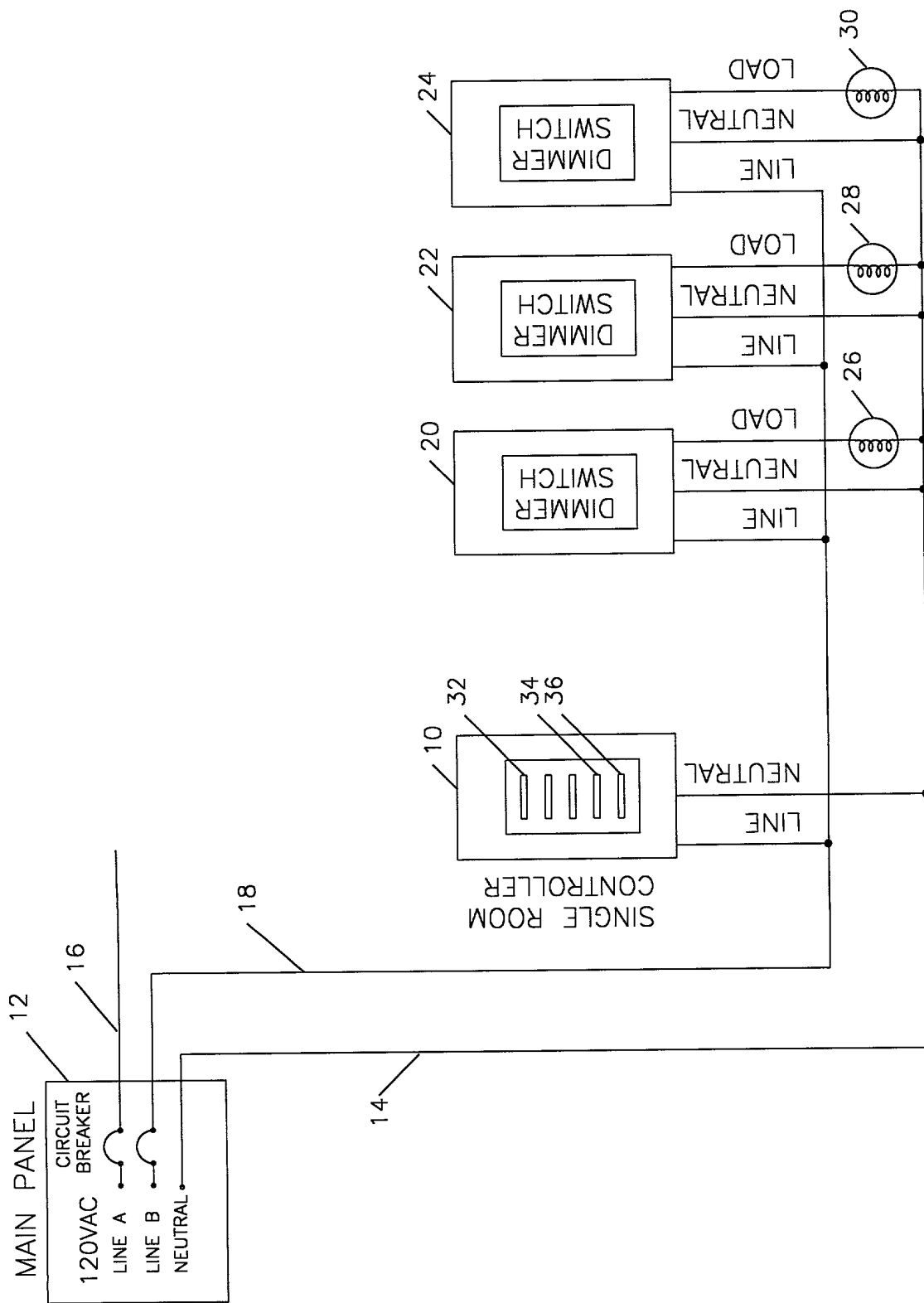


FIG. 2

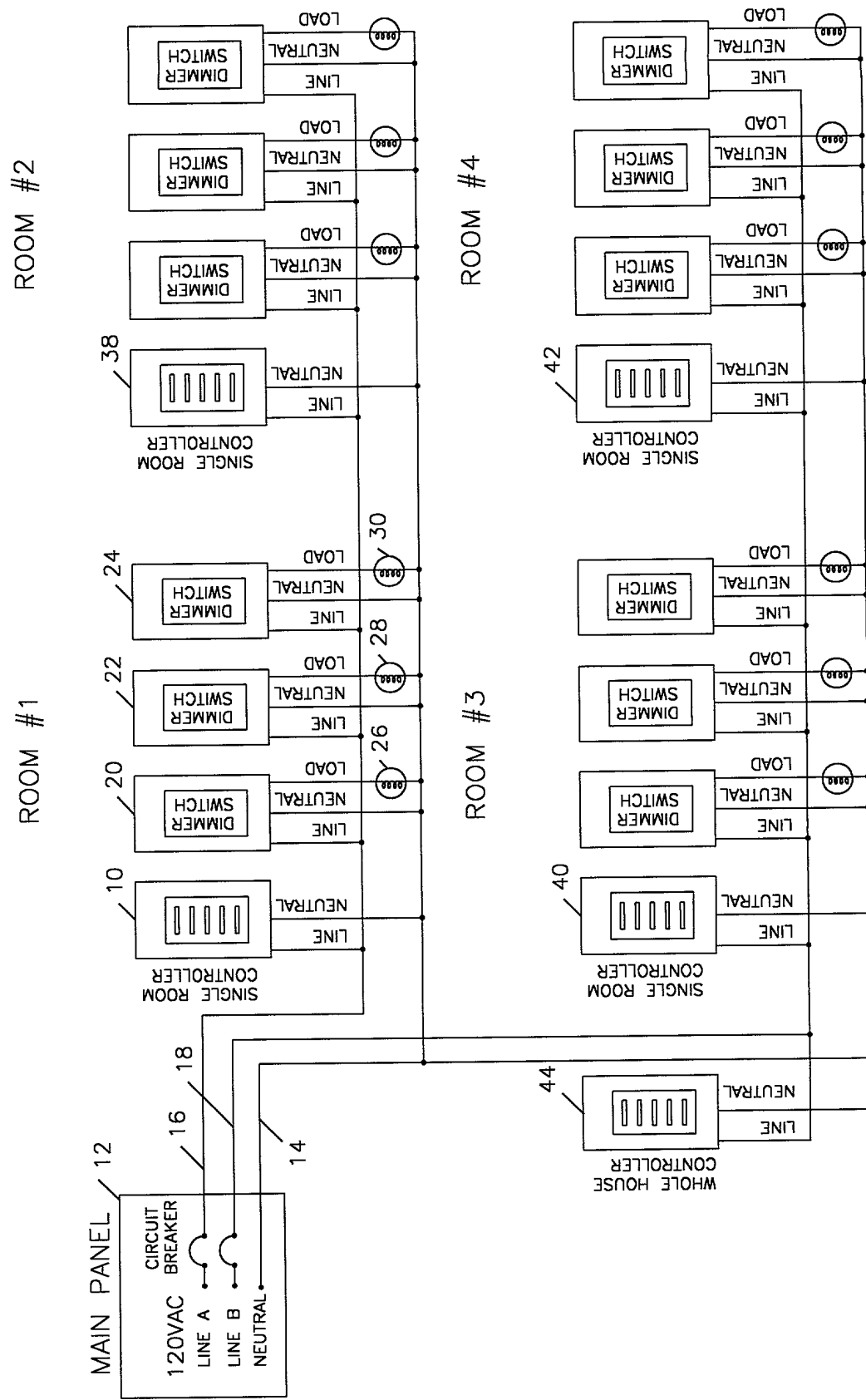


FIG. 3

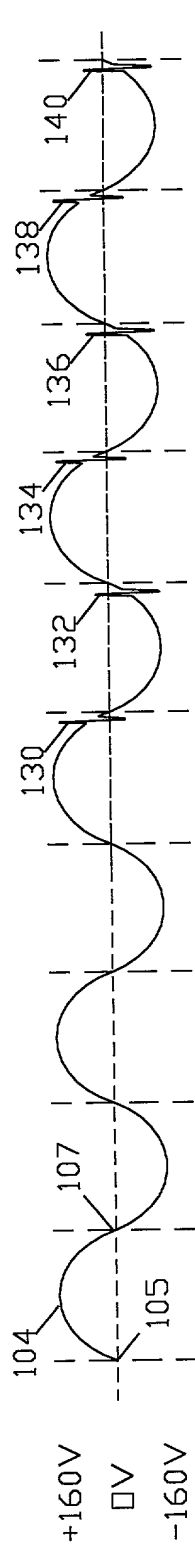


FIG. 4A

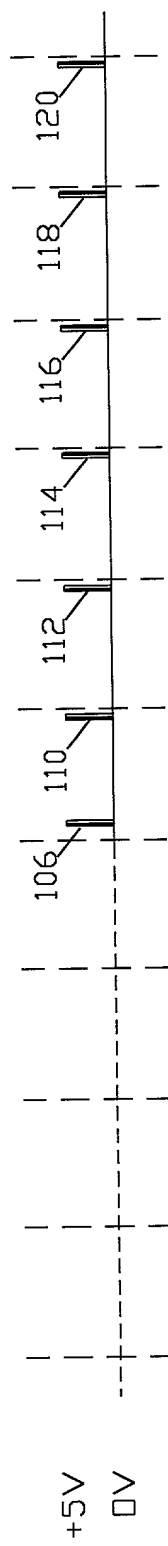


FIG. 4B

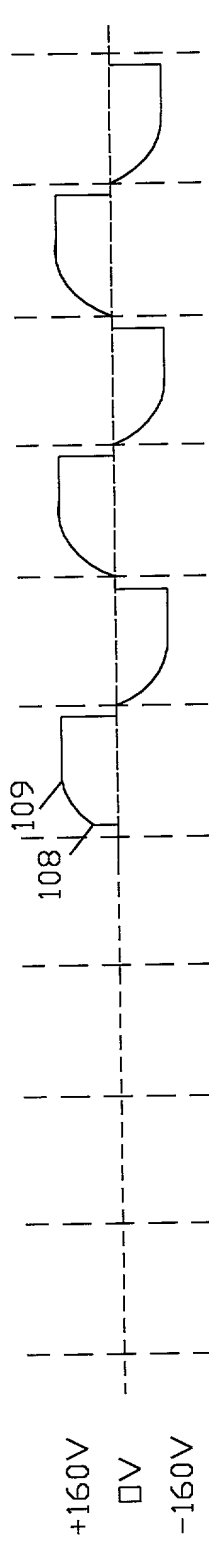


FIG. 4C

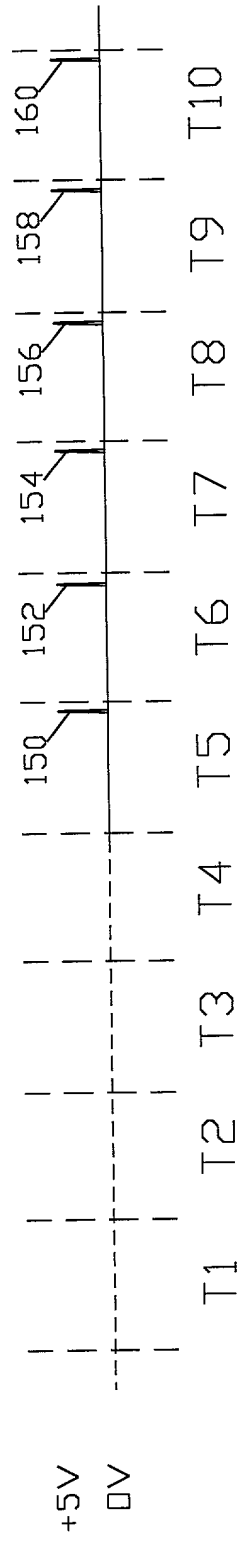
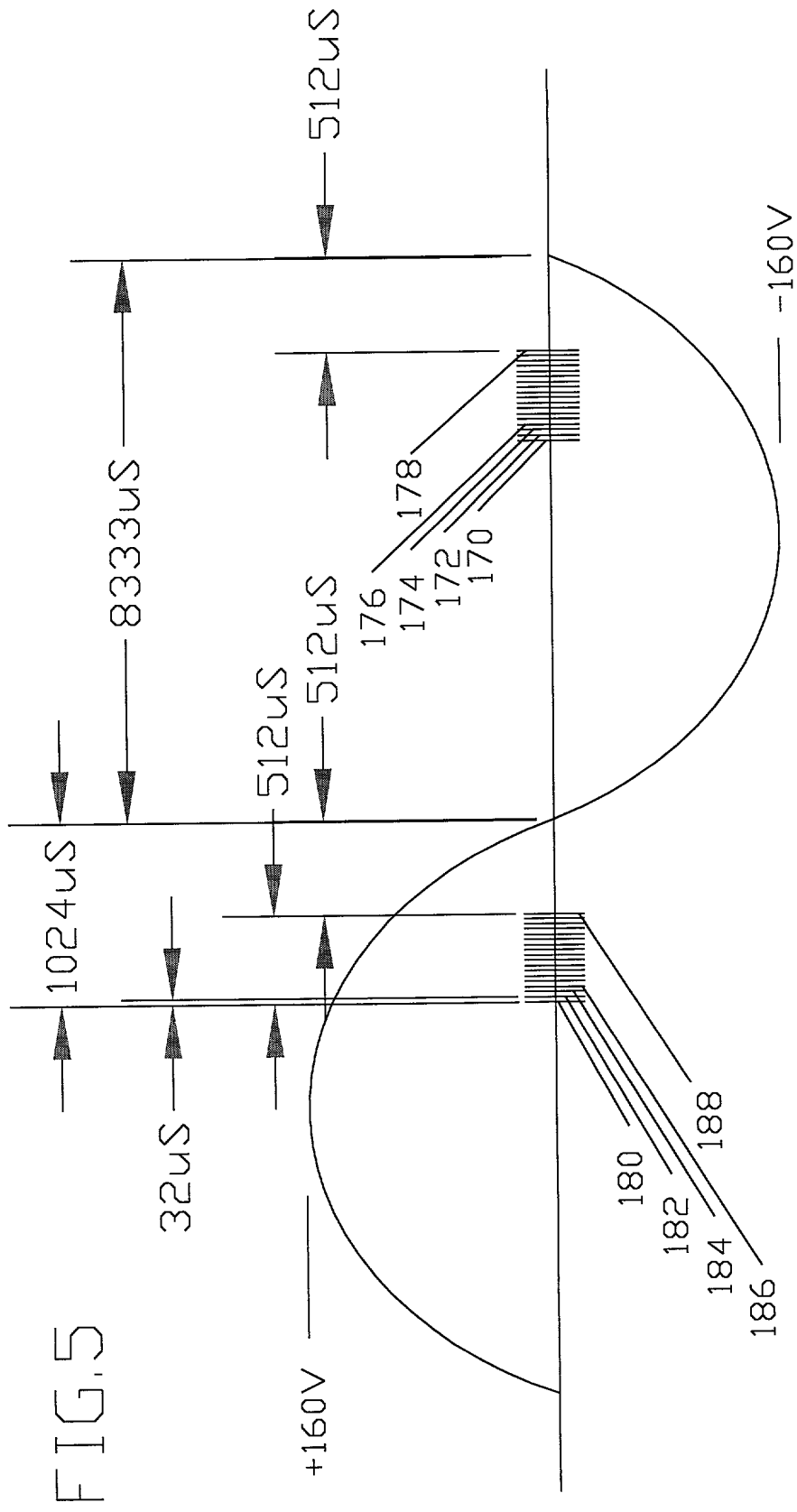


FIG. 4D



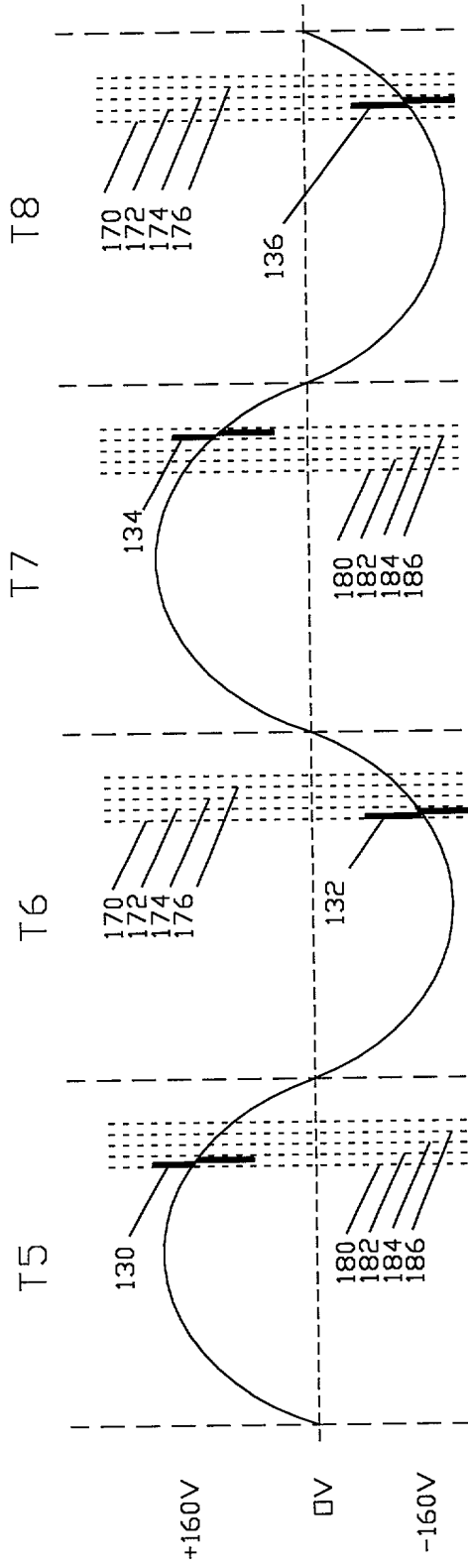


FIG. 6A

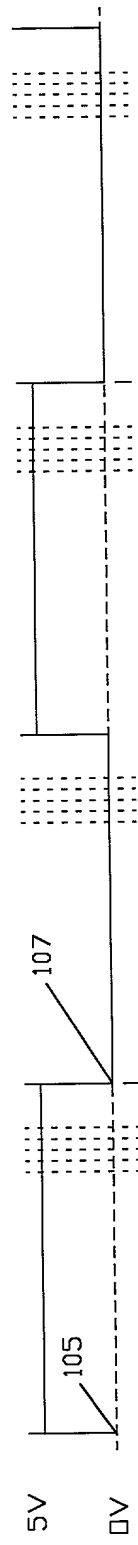


FIG. 6B

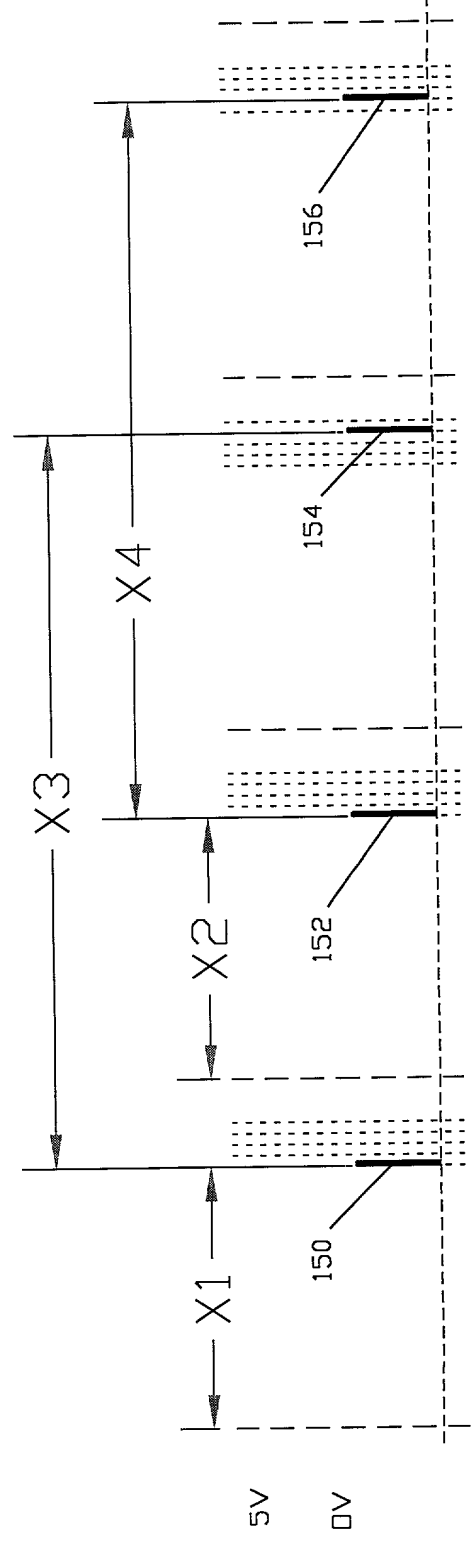


FIG. 6C

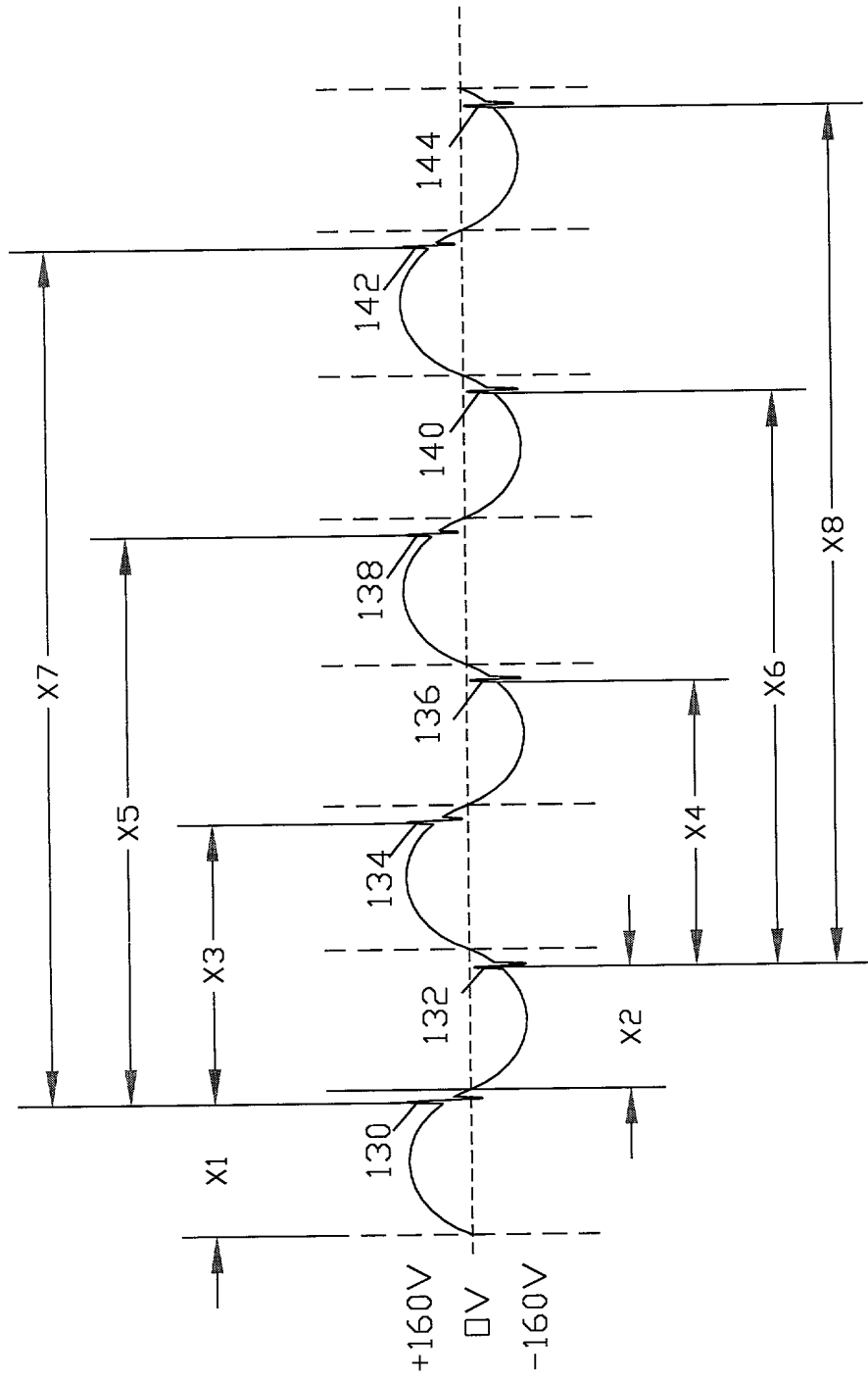


FIG. 7

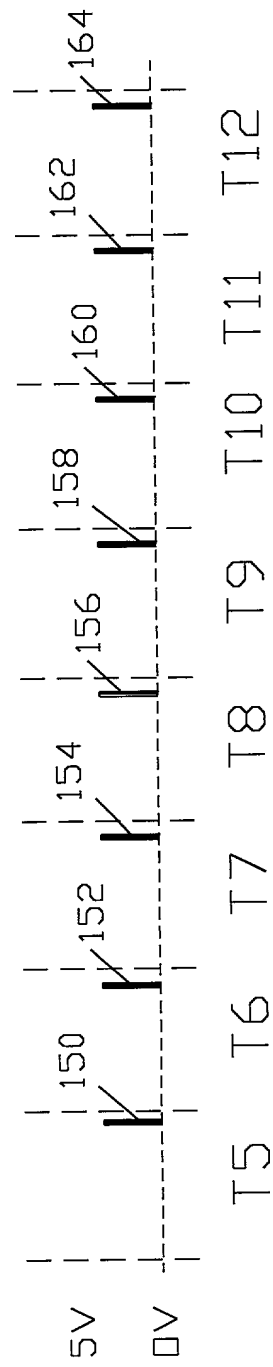


FIG. 8

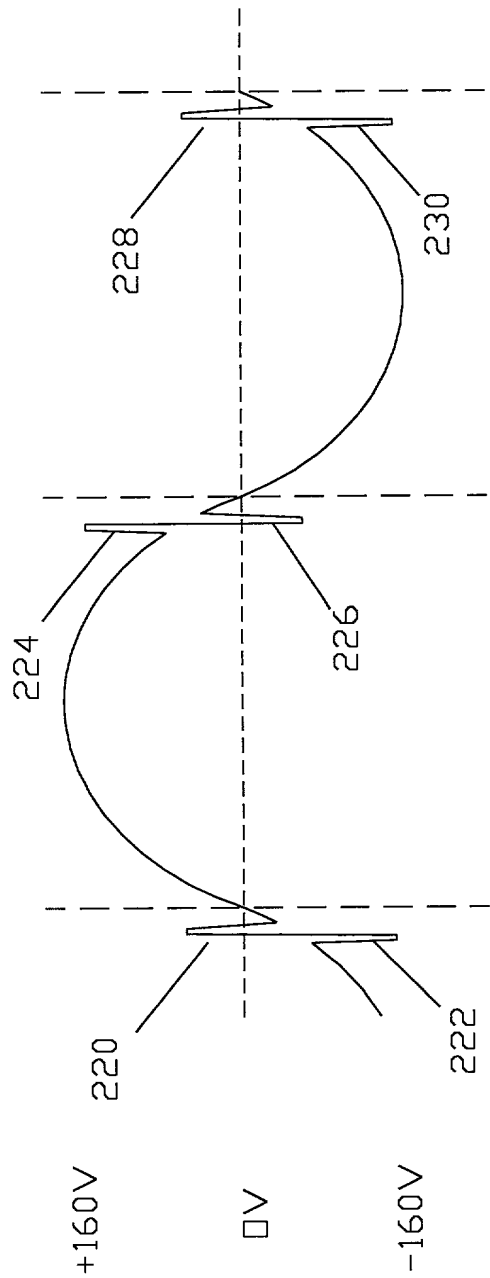


FIG. 9

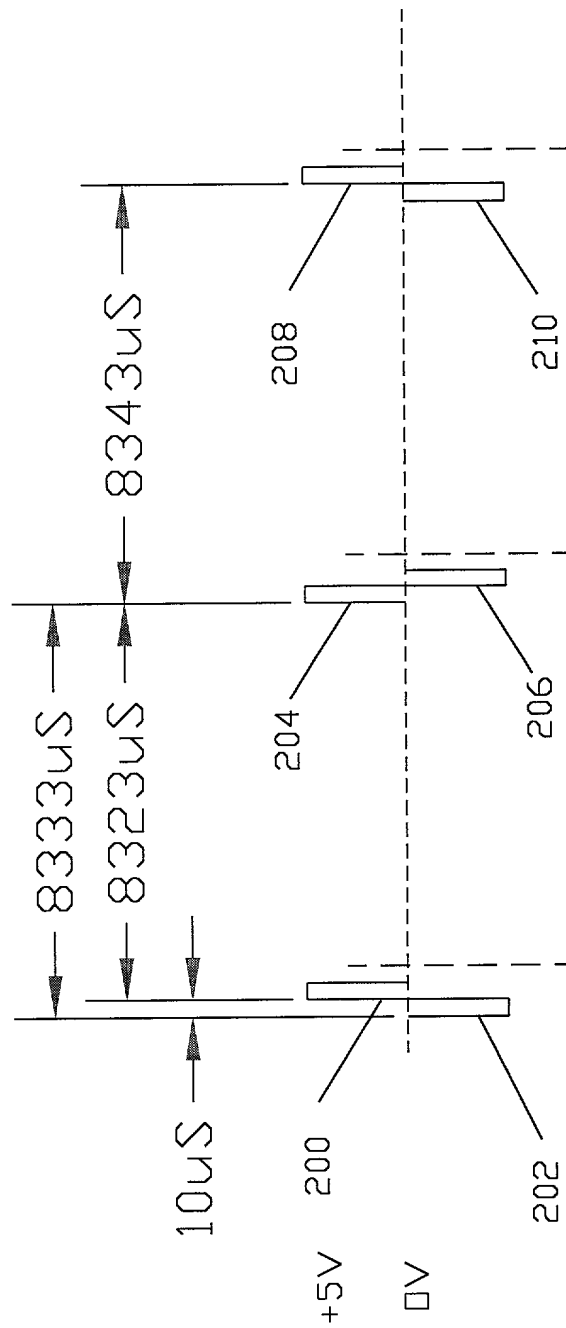


FIG. 10